

Amendments to the Claims:

Please amend claim 1, as follows:

1. (Currently Amended) A data recorder for writing data to a recording medium, the data recorder comprising:

a buffer memory for temporarily storing data before the data is written to the recording medium;

a buffer underrun determination circuit connected to the buffer memory, for deciding whether the buffer memory is in a state in which a buffer underrun will occur and whether the buffer memory is in a state in which a buffer underrun will no longer occur; and

a recording controller connected to the buffer memory and the buffer underrun determination circuit, wherein the recording controller controls interruption and restart of data writing based on the determination of the buffer underrun determination circuit, wherein the recording controller includes:

an encoder connected to the buffer memory, for encoding data which is read data from the buffer memory to generate recording data;

a clock generator connected to the encoder, for generating a system clock and providing the system clock to the encoder to operate the encoder;

a decoder connected to the clock generator, for decoding the data written on the recording medium to generate decoded data; and

a system control circuit connected to the encoder, the clock generator, and the decoder, for deciding whether the encoding of the encoder and the decoding of the decoder are synchronized and starting to write the recording data to the recording medium from the encoder when the encoding of the encoder and the decoding of the decoder are synchronized, subsequent to the interruption of the recording of data, wherein, the clock generator suspends ~~to provide~~ providing the system clock to the encoder until the decoding catches up with the encoding, when the decoding of the decoder is delayed from the encoding of the encoder.

2. (Previously Presented) The data recorder according to claim 1, wherein the clock generator generates a first system clock in accordance with the decoding of the decoder, generates a second system clock based on a reference clock having a predetermined frequency, provides the first system clock to the encoder until reaching an interrupted position, and provides the second system clock to the encoder after reaching the interrupted position.

3. (Original) The data recorder according to claim 2, wherein the decoder generates a pit clock based on the decoded data, and the clock generator generates the first system clock based on the pit clock.

4. (Original) The data recorder according to claim 3, wherein the clock generator includes a phase-locked loop (PLL) circuit connected to the decoder, wherein the PLL circuit generates the first system clock and the second system clock and selectively outputs the first and second system clocks.

5. (Original) The data recorder according to claim 3, wherein the clock generator includes:

a first PLL circuit connected to the decoder to generate a first system clock;

a second PLL circuit for generating a second system clock based on a reference clock;

and

a clock control circuit connected to the first and second PLL circuits, wherein the clock control circuit selectively provides the first and second system clocks to the encoder.

6. (Original) The data recorder according to claim 5, further comprising:

a recording unit connected to the encoder to write the recording data to the recording medium; and

a reading unit connected to the decoder to read the data written on the recording medium and generate read data.

7. (Previously Presented) A method for writing data on a recording medium, the method comprising:

encoding data to generate first encoded data;

writing the first encoded data to the recording medium;

reproducing the data written to the recording medium to generate reproduction data when the writing of data is interrupted;

encoding data corresponding to the data written on the recording medium to generate second encoded data;

suspending the generation of the second encoded data when the reproduction data is delayed from the second encoded data;

restarting the generation of the second encoded data when the reproduction data catches up with the second encoded data; and

restarting the recording of data at the moment the reproduction data and the second encoded data reach the data at which the writing of data was interrupted.

8. (Previously Presented) A method for controlling interruption and restart of writing data to a recording medium, wherein the data is stored in a buffer memory, the method comprising:

generating reproduction data, when the writing of data to the recording medium is interrupted, by sequentially reading the data recorded on the recording medium prior to the writing interruption;

generating recording data, when the recording of data to the recording medium is interrupted, by sequentially reading the data stored in the buffer memory;

suspending the generation of the recording data when the reproduction data is delayed from the recording data;

restarting the generation of the recording data when the delayed reproduction data catches up with the recording data;

Applicant : Koji Hayashi et al.
Serial No. : 09/748,504
Filed : December 26, 2000
Page : 6 of 8

Attorney's Docket No.: 10449-
031001 / P1S2000221US

and

restarting the recording of data at the moment the reproduction data and the recording data reach the data at which the writing of data was interrupted.